

Notice of References Cited	Application/Control No.	Applicant(s)/Patent Under Reexamination	
	09/714,524	CHARPENTIER ET AL.	
Examiner	Art Unit		Page 1 of 1
Brook Kebede	2823		

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,362,356	11-1994	Schoenborn, Philippe	216/60
	B	US-5,403,433	04-1995	Morrison et al.	216/60
	C	US-5,450,205	09-1995	Sawin et al.	356/632
	D	US-5,807,761	09-1998	Coronel et al.	438/14
	E	US-5,955,139	09-1999	Iturralde, Armando	427/9
	F	US-6,081,334	06-2000	Grimbergen et al.	356/499
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI era, Volume I, pp. 234-238, (1986).
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.